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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Tim Tuan et al.  
Assignee: Xilinx, Inc.  
Title: "Tuning Programmable Logic Devices for Low-Power Design Implementation"  
Serial No.: 10/783,216 Filing Date: 02/20/2004  
Examiner: Nelson C. Lam Art Unit: 2825  
Docket No.: X-1462-1P US Conf. No.: 6678

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**AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION**

In response to the final rejection in the Second Office Action mailed from the Patent Office on September 11, 2006, Applicants submit the following amendments and remarks.

Amendments to the Claims begin on page 2 of this paper.  
Remarks begin on page 7 of this paper.